DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE – RAIGAD -402 103

Winter Semester Examination(Supplementary) – Nov - 2019

Branch:- Electronics & Telecommunication Sem.:- Subject with Subject Code:- Microprocessor (BTEXC403) Marks		· IV
		ks:-60
Date:-	30/11/2019 Time:-3 Hr.	5y_
1 2 3 2	ions to the Students . Each question carries 12 marks. . Attempt any five questions of the following. . Illustrate your answers with neat sketches, diagram etc., wherever necessary. . If some part or parameter is noticed to be missing, you may appropriately assume it and nention it clearly	l should
Q.1.		
A	Explain internal architecture of 8085 with neat diagram.	(8)
В	What is addressing mode? Explain addressing modes of 8085 with example.	(4)
Q.2.		
Α	Explain Arithmetic group of instructions in 8085 with examples?	(6)
В	Explain memory write cycle for 8085 with suitable waveform.	(6)
Q.3.		(6)
A B	Explain types of interrupts. How multiple interrupts are handled in microprocessor with Programmable Interrupt Controller (PIC) 8259? Explain with suitable interfacing diagram.	(6)
Q.4.		
A	Explain pins function of following in detail i)ALE ii)HOLD	(6)
В	Write assembly program for 8085 to add the 16-bit number in memory locations 2501H and 2502H to the 16-bit number in memory locations 2503H and 2504H. The most significant eight bits of the two numbers to be added are in memory locations 2502H and 4004H. Store the result in memory locations 2505H and 2506H with the most significant byte in memory location 2506H.	[
Q.5.	The The State of the Timer with block diagram	(6)
A B	Explain Programmable Interval Timer with block diagram. What is stack memory? Explain PUSH and POP operation using suitable diagram.	(6)
Q.6.	The state of the s	(8)
Α	Explain detailed Architecture of 8086. What are Assembler directive? Explain with example.	(4)