DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE – RAIGAD -402 103

Mid Semester Examination – March - 2019

Branch: B.Tech (Electronics& Telecommunication) So	em.:- IV	
Subject : Microprocessor Subject Code:- BTEXC403		
Date:- 13 /03/2019 Time:- 1 Hr.	Marks: 20	
Instructions:- Assume data if required	Montra	(00)
	Marks)	(co)
Q.No.1 Fill in the blanks	(06)
1. 8085 has address lines		
a. 8 b.16 c.18 d.32		
2. How much memory can be accessed by 8085		
a. 2 k byte b. 1 k byte c. 3 k byte d. none of these		
3. MOV A,B instruction is used to transfer data from a. B to A b. A to B c. A to A d. none of these		
4. ANI instruction is used to		
a. Masking b. Unmasking c. both a and b d. none of these		
5. how many bit data bus 8085 is		
a. 8 bit b. 16 bit c. 32 bit d. 64 bit		
6. ALE pin of 8085 is		
a. Address latch enable b. arithmetic logic enable c. address logic enable d. none of these.	;	
u. none of these.		
Q.No. 2 Solve any two of the following:	(3x2)	
A.) Explain addressing modes with example in detail	(SAZ)	co (1)
B.) Subtract two 8 bit numbers available in reg B and reg C, resu	lt	(-)
will be stored in 2201H memory location		co (2)
C.) Write an instructions to i. load 00H in accumulator ii.		co(2)
Decrament accumulator iii. Move content of accumulator to		
Reg B		
Q.No.3 Solve any one of the following	8	
(A) Write an assembly language program to find largest number fro	_	
series of numbers available in memory location from 2301H		co(2)
on words, store result in 2400H.		` '
(B) What is interrupt? what are different types of interrupts? Expla	in	
RIM and SIM instructions.		co(4)